SIEMENS

Data sheet

6ES7512-1CK01-0AB0



SIMATIC S7-1500 Compact CPU CPU 1512C-1 PN, central processing unit with working memory 250 KB for program and 1 MB for data, 32 digital inputs, 32 digital outputs, 5 analog inputs, 2 analog outputs, 6 high speed counters, 4 high speed outputs for PTO/PWM/frequency output 1. interface: PROFINET IRT with 2 port switch, 48 NS bit-performance, incl. front connector push-in, SIMATIC memory card necessary

| version configurable as 6ES7512-1CK00-0AB0 Configuration control via dataset Via dataset Yes Display Screen diagonal [cm] Screen diagonal [cm] 3.45 cm Control elements Number of keys Number of keys 8 Mode buttons 2 Supply voltage Rated value (DC) permissible range, lower limit (DC) 24 V permissible range, lower limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 0.8 A; Without load; 18.8 A: CPU + load Current consumption (rated value) 0.8 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value Pt 0.34 A²-s Digital inputs - • from load voltage L+ (without load), max. 20 mA; Per group, without load Digital outputs - • from load voltage L+, max. 30 mA; Per group, without load Output voltage / header 24 V | General information | |
|---|---|--|
| Firmware version V2.9 Product function | Product type designation | CPU 1512C-1 PN |
| Product function • I&M data Yes; I&M0 to I&M3 • Isochronous mode Yes; With minimum OB 6x cycle of 625 µs (distributed) Engineering with V17 (FW V2.9) / V15 (FW V2.5) or higher; with older TIA Portal versio configuration control via dataset Yes Display Screen diagonal [cm] Screen diagonal [cm] 3.45 cm Control elements Number of keys Mode buttons 2 Supply voltage V17 (FW V2.9) / V10 (FW V2.5) or higher; with older TIA Portal versio configuration control via dataset Yes Display Screen diagonal [cm] Screen diagonal [cm] 3.45 cm Control elements 8 Number of keys 8 Mode buttons 2 Supply voltage V17 (FW V2.0) / V10 (FW V2.0) (FW V | HW functional status | FS03 |
| • I&M data Yes; I&M0 to I&M3 • Isochronous mode Yes; With minimum OB 6x cycle of 625 µs (distributed) Engineering with • STEP 7 TIA Portal configurable/integrated from version V17 (FW V2.9) / V15 (FW V2.5) or higher; with older TIA Portal versio configurable as 6ES7512-1CK00-0AB0 Configuration control via dataset Yes via dataset Yes Display Screen diagonal [cm] 3.45 cm Control elements Number of keys 8 Mode buttons 2 Supply voltage Rated value (DC) 24 V permissible range, upper limit (DC) 28.8 V Perverse polarity protection Yes Sm; Refers to the power supply on the CPU section • Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 0.8 A; Without load; 19 A: CPU + load Current consumption (rated value) 0.8 A; Without load; 19 A: CPU + load Invish current, max. 1.9 A; Rated value • from load voltage L+ (without load), max. 20 mA; per group Digital inputs • from load voltage L+, max. 30 mA; Per group, without load • form load voltage L+, max. | Firmware version | V2.9 |
| • Isochronous mode Yes; With minimum OB 6x cycle of 625 µs (distributed) Engineering with V17 (FW V2.9) / V15 (FW V2.5) or higher; with older TIA Portal version configurable as 6527512-1CK00-0ABO Configuration control via dataset Via dataset Yes Display Screen diagonal [cm] Screen diagonal [cm] 3.45 cm Control elements 8 Number of keys 8 Mode buttons 2 Supply voltage 24 V Permissible range, lower limit (DC) 19.2 V; 20.4 V DC, for supplying the digital inputs/outputs permissible range, upper limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current Current consumption (rated value) Current consumption, max. 1 A; Without load; 18.8 A: CPU + load Invush current, max. 19.4; Rated value It 0.34 A ² ·s Digital inputs 20 mA; per group output voltage I+, max. 30 mA; Per group, without load output voltage I heador 24 V | Product function | |
| Engineering with • STEP 7 TIA Portal configurable/integrated from version V17 (FW V2.9) / V15 (FW V2.5) or higher; with older TIA Portal versio configurable as 6ES7512-1CK00-0AB0 Configuration control via dataset Yes Display Screen diagonal [cm] 3.45 cm Control elements Number of keys 8 Number of keys 8 2 Supply voltage 2 Supply voltage Rated value (DC) 24 V 9 (20 × 20 × 4) VDC, for supplying the digital inputs/outputs permissible range, lower limit (DC) 28.8 V Reverse polarity protection Permissible range, lower limit (DC) 28.8 V Reverse polarity protection Ves Mains buffering 5 ms; Refers to the power supply on the CPU section • Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 0.8 A; Without load; 18.8 A: CPU + load Current consumption, max. 1 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value • from load voltage L+, max. 30 mA; Per group Digital inputs 6 from load voltage L+, max. • from load voltage L+, max. </td <td> I&M data </td> <td>Yes; I&M0 to I&M3</td> | I&M data | Yes; I&M0 to I&M3 |
| • STEP 7 TIA Portal configurable/integrated from version V17 (FW V2.9) / V15 (FW V2.5) or higher; with older TIA Portal version configurable as 6ES7512-1CK00-0AB0 Configuration control via dataset Yes Display 3.45 cm Control elements Number of keys 8 Mode buttons 2 Supply voltage 2 Supply voltage Rated value (DC) 24 V Permissible range, lower limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 5 ms; Refers to the power supply on the CPU section Yes Mains buffering 1/s 1/s • Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section 1/s Input current Urrent consumption (rated value) 0.8 A; Without load; 18.8 A: CPU + load 1/s A: Without load; 19 A: CPU + load Inrush current, max. 1,9 A; Rated value 0.34 A ² ·s Digital inputs • from load voltage L+ (without load), max. 20 mA; per group 20 mA; Per group, without load Digital outputs - - - - Pated value (DC) 24 V 24 V - - | Isochronous mode | Yes; With minimum OB 6x cycle of 625 µs (distributed) |
| version configurable as 6ES7512-1CK00-0AB0 Control Ves Display Screen diagonal [cm] 3.45 cm Control elements 8 Number of keys 8 Mode buttons 2 Supply voltage 2 Rated value (DC) 24 V permissible range, lower limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering - • Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current - Current consumption (rated value) 0.8 A; Without load; 18.8 A: CPU + load Current consumption, max. 1 A; Without load; 19 A: CPU + load Inrush current, max. 0.34 A²-s Digital inputs - • from load voltage L+ (without load), max. 20 mA; per group, without load Digital outputs - - • from load voltage L+, max. 30 mA; Per group, without load Output voltage / header - - | Engineering with | |
| via dataset Yes Display Screen diagonal [cm] 3.45 cm Control elements Number of keys 8 Mode buttons 2 Supply voltage Rated value (DC) 24 V permissible range, lower limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 5 ms; Refers to the power supply on the CPU section • Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 2 Current consumption (rated value) 0.8 A; Without load; 18.8 A: CPU + load Current consumption, max. 1 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value I*t 0.34 A²-s Digital inputs 0 mA; Per group • from load voltage L+, max. 30 mA; Per group, without load output voltage / header 24 V | | V17 (FW V2.9) / V15 (FW V2.5) or higher; with older TIA Portal versions configurable as 6ES7512-1CK00-0AB0 |
| Display Screen diagonal [cm] 3.45 cm Control elements Number of keys 8 Mode buttons 2 Supply voltage 2 Rated value (DC) 24 V permissible range, lower limit (DC) 29.2 V; 20.4 V DC, for supplying the digital inputs/outputs permissible range, lower limit (DC) 28.8 V Reverse polarity protection Yes Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current Current consumption, max. Current consumption, max. 1.9 A; Rated value Inrush current, max. 1.9 A; Rated value It 0.34 A²-s Digital inputs 0 mA; per group • from load voltage L+ (without load), max. 20 mA; per group Digital outputs • from load voltage L+, max. • from load voltage L+, max. 30 mA; Per group, without load Output voltage / header 24 V | Configuration control | |
| Screen diagonal [cm] 3.45 cm Control elements 8 Number of keys 8 Mode buttons 2 Supply voltage 2 Rated value (DC) 24 V permissible range, lower limit (DC) 29.2 V; 20.4 V DC, for supplying the digital inputs/outputs permissible range, upper limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 0.8 A; Without load; 18.8 A: CPU + load Current consumption, max. 1 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value I* 0.34 A²-s Digital inputs 20 mA; per group • from load voltage L+ (without load), max. 20 mA; per group • form load voltage L+, max. 30 mA; Per group, without load output voltage / header 24 V | via dataset | Yes |
| Control elements Number of keys 8 Mode buttons 2 Supply voltage 2 Rated value (DC) 24 V permissible range, lower limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 6 Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section Input current 0.8 A; Without load; 18.8 A: CPU + load Current consumption (rated value) 0.8 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value I*t 0.34 A²-s Digital inputs 0 mA; per group Digital outputs 30 mA; Per group, without load output voltage L+ (max. 30 mA; Per group, without load Output voltage / header 24 V | Display | |
| Number of keys 8 Mode buttons 2 Supply voltage 24 V Rated value (DC) 24 V permissible range, lower limit (DC) 19.2 V; 20.4 V DC, for supplying the digital inputs/outputs permissible range, upper limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 6 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 0.8 A; Without load; 18.8 A: CPU + load Current consumption (rated value) 0.8 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value I*t 0.34 A²-s Digital inputs • from load voltage L+ (without load), max. • from load voltage L+, max. 30 mA; Per group, without load output voltage / header 24 V | Screen diagonal [cm] | 3.45 cm |
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| Rated value (DC) 24 V permissible range, lower limit (DC) 19.2 V; 20.4 V DC, for supplying the digital inputs/outputs permissible range, upper limit (DC) 28.8 V Reverse polarity protection Yes Mains buffering 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 0.8 A; Without load; 18.8 A: CPU + load Current consumption (rated value) 0.8 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value Igital inputs 0.34 A²-s Digital inputs 20 mA; per group Digital outputs 30 mA; Per group, without load output voltage / header 24 V | Mode buttons | 2 |
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| Mains buffering 5 ms; Refers to the power supply on the CPU section • Mains/voltage failure stored energy time 5 ms; Refers to the power supply on the CPU section • Repeat rate, min. 1/s Input current 0.8 A; Without load; 18.8 A: CPU + load Current consumption (rated value) 0.8 A; Without load; 19 A: CPU + load Current, max. 1.9 A; Rated value Inrush current, max. 0.34 A ² ·s Digital inputs 20 mA; per group • from load voltage L+ (without load), max. 20 mA; per group, without load Output voltage / header 30 mA; Per group, without load Rated value (DC) 24 V | permissible range, upper limit (DC) | 28.8 V |
| Mains/voltage failure stored energy time Repeat rate, min. Input current Current consumption (rated value) Current consumption, max. Inrush current, max. Inrush current, max. Pate value A; Rated value A²·s Digital inputs from load voltage L+ (without load), max. Max. Max. A; Per group, without load A; Per group, without load A; Per group, without load | Reverse polarity protection | Yes |
| Repeat rate, min. Input current Current consumption (rated value) O.8 A; Without load; 18.8 A: CPU + load Current consumption, max. I A; Without load; 19 A: CPU + load Inrush current, max. I.9 A; Rated value I²t O.34 A²·s Digital inputs from load voltage L+ (without load), max. 20 mA; per group Digital outputs from load voltage L+, max. 30 mA; Per group, without load output voltage / header Rated value (DC) 24 V | Mains buffering | |
| Input current Current consumption (rated value) 0.8 A; Without load; 18.8 A: CPU + load Current consumption, max. 1 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value I²t 0.34 A²·s Digital inputs 20 mA; per group • from load voltage L+ (without load), max. 20 mA; per group Digital outputs 30 mA; Per group, without load • from load voltage L+, max. 30 mA; Per group, without load Output voltage / header 24 V | Mains/voltage failure stored energy time | 5 ms; Refers to the power supply on the CPU section |
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| Current consumption, max. 1 A; Without load; 19 A: CPU + load Inrush current, max. 1.9 A; Rated value I²t 0.34 A²·s Digital inputs 20 mA; per group • from load voltage L+ (without load), max. 20 mA; per group Digital outputs 30 mA; Per group, without load • from load voltage L+, max. 30 mA; Per group, without load Output voltage / header 24 V | Input current | |
| Inrush current, max. 1.9 A; Rated value I²t 0.34 A²·s Digital inputs 20 mA; per group • from load voltage L+ (without load), max. 20 mA; per group Digital outputs 30 mA; Per group, without load • from load voltage L+, max. 30 mA; Per group, without load output voltage / header 24 V | Current consumption (rated value) | 0.8 A; Without load; 18.8 A: CPU + load |
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| Digital inputs • from load voltage L+ (without load), max. 20 mA; per group Digital outputs • from load voltage L+, max. 30 mA; Per group, without load output voltage / header Rated value (DC) 24 V | Inrush current, max. | 1.9 A; Rated value |
| from load voltage L+ (without load), max. 20 mA; per group Digital outputs from load voltage L+, max. 30 mA; Per group, without load output voltage / header Rated value (DC) 24 V | l²t | 0.34 A ² ·s |
| Digital outputs • from load voltage L+, max. 30 mA; Per group, without load output voltage / header Rated value (DC) 24 V | Digital inputs | |
| from load voltage L+, max. 30 mA; Per group, without load output voltage / header Rated value (DC) 24 V | from load voltage L+ (without load), max. | 20 mA; per group |
| output voltage / header Rated value (DC) 24 V | | |
| Rated value (DC) 24 V | | 30 mA; Per group, without load |
| | output voltage / header | |
| | | 24 V |
| Encoder supply | Encoder supply | |
| Number of outputs 2; One common 24 V encoder supply per 16 digital inputs | Number of outputs | 2; One common 24 V encoder supply per 16 digital inputs |
| 24 V encoder supply | | |
| • 24 V Yes; L+ (-0.8 V) | • 24 V | Yes; L+ (-0.8 V) |

| Short-circuit protection | Yes |
|---|---|
| Output current, max. | 1A |
| Power | |
| Infeed power to the backplane bus | 10 W |
| Power consumption from the backplane bus (balanced) | 9 W |
| Power loss | |
| Power loss, typ. | 15.2 W |
| Memory | 13.2 W |
| | 4 |
| Number of slots for SIMATIC memory card SIMATIC memory card required | 1 Yes |
| Work memory | 105 |
| integrated (for program) | 250 kbyte |
| • integrated (for data) | 1 Mbyte |
| Load memory | |
| Plug-in (SIMATIC Memory Card), max. | 32 Gbyte |
| Backup | |
| maintenance-free | Yes |
| CPU processing times | |
| for bit operations, typ. | 48 ns |
| for word operations, typ. | 58 ns |
| for fixed point arithmetic, typ. | 77 ns |
| for floating point arithmetic, typ. | 307 ns |
| CPU-blocks | |
| Number of elements (total) | 4 000; Blocks (OB, FB, FC, DB) and UDTs |
| DB | |
| Number range | 1 60 999; subdivided into: number range that can be used by the user: 1 59 999, and number range of DBs created via SFC 86: 60 000 |
| | 60 999 |
| • Size, max. | 1 Mbyte; For DBs with absolute addressing, the max. size is 64 KB |
| FB | |
| Number range | 0 65 535 |
| • Size, max. | 250 kbyte |
| FC | |
| Number range | 0 65 535 |
| • Size, max. OB | 250 kbyte |
| • Size, max. | 250 kbyte |
| | 100 |
| Number of free cycle OBs | |
| Number of free cycle OBs Number of time alarm OBs | |
| Number of time alarm OBs | 20 20 |
| - | 20 |
| Number of time alarm OBsNumber of delay alarm OBs | 20 20 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs | 20 20 20; With minimum OB 3x cycle of 500 µs |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs | 20 20; With minimum OB 3x cycle of 500 µs 50 3 1 2 100 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of synchronous error OBs | 20 20; With minimum OB 3x cycle of 500 µs 50 3 1 2 100 4 2 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of synchronous error OBs Number of diagnostic alarm OBs | 20 20; With minimum OB 3x cycle of 500 µs 50 3 1 2 100 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of synchronous error OBs Number of diagnostic alarm OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 1 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class | 20 20; With minimum OB 3x cycle of 500 µs 50 3 1 2 100 4 2 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 1 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 1 2 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity S7 counter Number | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 1 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 1 2 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity S7 counter Number Retentivity | 20 20; With minimum OB 3x cycle of 500 µs 50 3 1 2 100 4 2 1 2 4 2 1 2 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity S7 counter Number Retentivity adjustable | 20 20; With minimum OB 3x cycle of 500 µs 50 3 1 2 100 4 2 1 2 4 2 1 2 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity S7 counter Number Retentivity adjustable IEC counter Number Retentivity | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 2 1 24 2 4 2 4 2 4 2 4 2 4 2 4 3 5 5 5 3 1 2 4 2 4 3 5 5 5 5 3 1 2 4 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity S7 counter Number Retentivity adjustable IEC counter Number Retentivity adjustable | 20 20; With minimum OB 3x cycle of 500 µs 50 3 1 2 100 4 2 1 2 4 2 4 2 4 2 4 2 4 2 4 2 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity S7 counter Number Retentivity adjustable IEC counter Number Retentivity adjustable S7 times | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 2 1 24 2 4 2 4 2 4 2 4 2 4 2 4 2 4 |
| Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of diagnostic alarm OBs Number of diagnostic alarm OBs Nesting depth per priority class Counters, timers and their retentivity S7 counter Number Retentivity adjustable IEC counter Number Retentivity adjustable | 20 20; With minimum OB 3x cycle of 500 μs 50 3 1 2 100 4 2 2 1 24 2 4 2 4 2 4 2 4 2 4 2 4 3 5 5 5 3 1 2 4 2 4 3 5 5 5 5 3 1 2 4 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 |

| — adjustable | Yes |
|---|--|
| IEC timer | |
| Number | Any (only limited by the main memory) |
| Retentivity | |
| — adjustable | Yes |
| Data areas and their retentivity | |
| Retentive data area (incl. timers, counters, flags), max. | 128 kbyte; In total; available retentive memory for bit memories, timers, counters, DBs, and technology data (axes): 88 KB |
| Extended retentive data area (incl. timers, counters, flags), max. | 1 Mbyte; When using PS 6 0W 24/48/60 V DC HF |
| Flag | |
| Size, max. | 16 kbyte |
| Number of clock memories Data blocks | 8; 8 clock memory bit, grouped into one clock memory byte |
| Retentivity adjustable | Yes |
| Retentivity preset | No |
| Local data | |
| per priority class, max. | 64 kbyte; max. 16 KB per block |
| Address area | |
| Number of IO modules | 2 048; max. number of modules / submodules |
| I/O address area | |
| Inputs | 32 kbyte; All inputs are in the process image |
| Outputs | 32 kbyte; All outputs are in the process image |
| per integrated IO subsystem | |
| — Inputs (volume) | 8 kbyte |
| — Outputs (volume) | 8 kbyte |
| per CM/CP | |
| — Inputs (volume) | 8 kbyte |
| — Outputs (volume) | 8 kbyte |
| Subprocess images | |
| Number of subprocess images, max. | 32 |
| Hardware configuration | |
| | |
| Number of distributed IO systems | 32; A distributed I/O system is characterized not only by the integration of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) |
| Number of distributed IO systems Number of DP masters | of distributed I/O via PROFINET or PROFIBUS communication |
| | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or |
| Number of DP masters | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can |
| Number of DP masters • Via CM | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day Clock | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day Clock • Type | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots Hardware clock |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. Operating hours counter | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically 10 s; Typ.: 2 s |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. Operating hours counter • Number | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically 10 s; Typ.: 2 s |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. • Number of lines, max. • Number of PtP CMs • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. Operating hours counter • Number Clock synchronization | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. • Number of lines, max. • Number of PtP CMs • Number of PtP CMs • Deviation per day, max. Operating hours counter • Number • Number | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically 10 s; Typ.: 2 s 16 Yes |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. • Number of lines, max. • Number of PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. Operating hours counter • Number Clock synchronization • supported • in AS, master | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically 10 s; Typ.: 2 s 16 Yes Yes Yes |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. • Number of lines, max. • Number of PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. Operating hours counter • Number Clock synchronization • supported • in AS, master • in AS, slave | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. • Number of lines, max. • Number of PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. Operating hours counter • Number Clock synchronization • supported • in AS, master • in AS, slave • on Ethernet via NTP | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots |
| Number of DP masters • Via CM Number of IO Controllers • integrated • Via CM Rack • Modules per rack, max. • Number of lines, max. • Number of lines, max. PtP CM • Number of PtP CMs Time of day Clock • Type • Backup time • Deviation per day, max. Operating hours counter • Number Clock synchronization • supported • in AS, master • in AS, slave • on Ethernet via NTP Digital inputs | of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 1 6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total 32; CPU + 31 modules 1 the number of connectable PtP CMs is only limited by the number of available slots |

| Source/sink input | P-reading |
|---|--|
| Input characteristic curve in accordance with IEC 61131, | Yes |
| type 3 | |
| Digital input functions, parameterizable | Vee |
| Gate start/stop | Yes |
| Capture | Yes |
| Synchronization | Yes |
| Input voltage | 20 |
| • Type of input voltage | DC |
| Rated value (DC) | 24 V |
| • for signal "0" | -3 to +5V |
| • for signal "1" | +11 to +30V |
| Input current | |
| for signal "1", typ. | 2.5 mA |
| Input delay (for rated value of input voltage) | |
| for standard inputs | |
| — parameterizable | Yes; none / 0.05 / 0.1 / 0.4 / 1.6 / 3.2 / 12.8 / 20 ms |
| — at "0" to "1", min. | 4 µs; for parameterization "none" |
| — at "0" to "1", max. | 20 ms |
| — at "1" to "0", min. | 4 μs; for parameterization "none" |
| — at "1" to "0", max. | 20 ms |
| for interrupt inputs | |
| — parameterizable | Yes; Same as for standard inputs |
| for technological functions | |
| — parameterizable | Yes; Same as for standard inputs |
| Cable length | |
| • shielded, max. | 1 000 m; 600 m for technological functions; depending on input |
| | frequency, encoder and cable quality; max. 50 m at 100 kHz |
| unshielded, max. | 600 m; for technological functions: No |
| Digital outputs | |
| Type of digital output | Transistor |
| integrated channels (DO) | 32 |
| Current-sourcing | Yes; Push-pull output |
| Short-circuit protection | Yes; electronic/thermal |
| Response threshold, typ. | 1.6 A with standard output, 0.5 A with high-speed output; see manual for |
| • Responde threshold, typ. | details |
| Limitation of inductive shutdown voltage to | Connector X11: -0.8 V; connector X12: L+ (-53 V) |
| Controlling a digital input | Yes |
| Accuracy of pulse duration | Up to ± 100 ppm ± 2 µs at high-speed output; see manual for details |
| minimum pulse duration | 2 µs; With High Speed output |
| Digital output functions, parameterizable | - p-, |
| Switching tripped by comparison values | Yes; As output signal of a high-speed counter |
| PWM output | Yes |
| - Number, max. | 4 |
| — Cycle duration, parameterizable | Yes |
| — ON period, min. | 0 % |
| | 100 % |
| - ON period, max. | |
| Resolution of the duty cycle | 0.0036 %; For S7 analog format, min. 40 ns |
| Frequency output | Yes |
| Switching capacity of the outputs | |
| with resistive load, max. | 0.5 A; 0.1 A with high-speed output, i.e. when using a high-speed output; see manual for details |
| e en lemp lead may | • |
| on lamp load, max. | 5 W; 1 W with high-speed output, i.e. when using a high-speed output; see manual for details |
| Load resistance range | |
| lower limit | 48 Ω ; 240 ohms with high-speed output, i.e. when using a high-speed |
| | output; see manual for details |
| ● upper limit | $12 \text{ k}\Omega$ |
| Output voltage | |
| Type of output voltage | DC |
| | |
| | |
| • for signal "0", max. | 1 V; With high-speed output, i.e. when using a high-speed output; see manual for details |
| | 1 V; With high-speed output, i.e. when using a high-speed output; see |
| • for signal "0", max. | 1 V; With high-speed output, i.e. when using a high-speed output; see manual for details |
| for signal "0", max. for signal "1", min. | 1 V; With high-speed output, i.e. when using a high-speed output; see manual for details |

| for signal "1" permissible range, min. for signal "1" permissible range, max. | output, observe derating; see manual for details 2 mA 0.6 A; 0.12 A with high-speed output, i.e. when using a high-speed output, observe derating; see manual for details |
|---|---|
| • for signal "0" residual current, max. | 0.5 mA |
| Output delay with resistive load | 000 |
| • "0" to "1", max. | 200 µs |
| • "1" to "0", max. | 500 µs; Load-dependent |
| for technological functions | |
| — "0" to "1", max. | 5 µs; Depending on the output used, see additional description in manual |
| — "1" to "0", max. | 5 µs; Depending on the output used, see additional description in manual |
| Parallel switching of two outputs | |
| for logic links | Yes; for technological functions: No |
| for uprating | No |
| for redundant control of a load | Yes; for technological functions: No |
| Switching frequency | |
| with resistive load, max. | 100 kHz; For high-speed output, 100 Hz for standard output |
| with inductive load, max. | 0.5 Hz; Acc. to IEC 60947-5-1, DC-13; observe derating curve |
| on lamp load, max. | 10 Hz |
| Total current of the outputs | |
| Current per channel, max. | 0.5 A; see additional description in the manual |
| Current per group, max. | 8 A; see additional description in the manual |
| Current per power supply, max. | 4 A; 2 power supplies for each group, current per power supply max. 4 A, see additional description in manual |
| for technological functions | |
| — Current per channel, max. | 0.5 A; see additional description in the manual |
| Relay outputs | |
| Number of relay outputs | 0 |
| Cable length | |
| shielded, max. | 1 000 m; 600 m for technological functions; depending on output frequency, load, and cable quality; max. 50 m at 100 kHz |
| unshielded, max. | COO may fain to also also also also also also also als |
| • unshielded, max. | 600 m; for technological functions: No |
| Analog inputs | 600 m; for technological functions: No |
| Analog inputs | |
| | 5; 4x for U/I, 1x for R/RTD 4; max. |
| Analog inputs Number of analog inputs | 5; 4x for U/I, 1x for R/RTD |
| Analog inputs Number of analog inputs For current measurement | 5; 4x for U/I, 1x for R/RTD 4; max. |
| Analog inputs Number of analog inputs For current measurement For voltage measurement For resistance/resistance thermometer | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 k Ω Yes; Physical measuring range: ± 10 V 100 k Ω |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ Yes |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ Yes 100 kΩ |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 k Ω Yes; Physical measuring range: ± 10 V |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ Yes 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) Input ranges (rated values), currents • 0 to 20 mA | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ Yes 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) Input ranges (rated values), currents • 0 to 20 mA — Input resistance (0 to 20 mA) | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ Yes 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) Input ranges (rated values), currents • 0 to 20 mA — Input resistance (0 to 20 mA) - 20 mA to +20 mA | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 10 V 100 kΩ |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) Input ranges (rated values), currents • 0 to 20 mA — Input resistance (0 to 20 mA) • -20 mA to +20 mA — Input resistance (-20 mA to +20 mA) | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 20 mA 50 Ω; Plus approx. 55 ohm for overvoltage protection by PTC Yes 50 Ω; Plus approx. 55 ohm for overvoltage protection by PTC |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) Input ranges (rated values), currents • 0 to 20 mA — Input resistance (0 to 20 mA) • -20 mA to +20 mA — Input resistance (-20 mA to +20 mA) • 4 mA to 20 mA | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 20 mA 50 Ω; Plus approx. 55 ohm for overvoltage protection by PTC Yes; Physical measuring range: ± 20 mA |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) Input ranges (rated values), currents • 0 to 20 mA — Input resistance (0 to 20 mA) • -20 mA to +20 mA — Input resistance (-20 mA to +20 mA) • 4 mA to 20 mA — Input resistance (4 mA to 20 mA) | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 20 mA 50 Ω; Plus approx. 55 ohm for overvoltage protection by PTC Yes 50 Ω; Plus approx. 55 ohm for overvoltage protection by PTC |
| Analog inputs Number of analog inputs • For current measurement • For voltage measurement • For resistance/resistance thermometer measurement permissible input voltage for voltage input (destruction limit), max. permissible input current for current input (destruction limit), max. Cycle time (all channels), min. Technical unit for temperature measurement adjustable Input ranges (rated values), voltages • 0 to +10 V — Input resistance (0 to 10 V) • 1 V to 5 V — Input resistance (1 V to 5 V) • -10 V to +10 V — Input resistance (-10 V to +10 V) • -5 V to +5 V — Input resistance (-5 V to +5 V) Input ranges (rated values), currents • 0 to 20 mA — Input resistance (0 to 20 mA) • -20 mA to +20 mA — Input resistance (-20 mA to +20 mA) • 4 mA to 20 mA | 5; 4x for U/I, 1x for R/RTD 4; max. 4; max. 1 28.8 V 40 mA 1 ms; Dependent on the parameterized interference frequency suppression; for details, see conversion procedure in manual Yes; °C/°F/K Yes; Physical measuring range: ± 10 V 100 kΩ Yes; Physical measuring range: ± 20 mA 50 Ω; Plus approx. 55 ohm for overvoltage protection by PTC Yes; Physical measuring range: ± 20 mA |

| Input registance (Ni 100) | 10 MO |
|---|---|
| — Input resistance (Ni 100) | 10 MΩ |
| Pt 100 Input registered (Bt 100) | Yes; Standard/climate |
| — Input resistance (Pt 100) | 10 ΜΩ |
| Input ranges (rated values), resistors • 0 to 150 ohms | Yes; Physical measuring range: 0 600 ohms |
| - Input resistance (0 to 150 ohms) | 10 M Ω |
| • 0 to 300 ohms | Yes; Physical measuring range: 0 600 ohms |
| — Input resistance (0 to 300 ohms) | 10 M Ω |
| • 0 to 600 ohms | Yes |
| — Input resistance (0 to 600 ohms) | 10 MΩ |
| Cable length | |
| • shielded, max. | 800 m; for U/I, 200 m for R/RTD |
| Analog outputs | |
| integrated channels (AO) | 2 |
| Voltage output, short-circuit protection | Yes |
| Cycle time (all channels), min. | 1 ms; Dependent on the parameterized interference frequency |
| | suppression; for details, see conversion procedure in manual |
| Output ranges, voltage | |
| • 0 to 10 V | Yes |
| • 1 V to 5 V | Yes |
| • -10 V to +10 V | Yes |
| Output ranges, current | |
| • 0 to 20 mA | Yes |
| • -20 mA to +20 mA | Yes |
| • 4 mA to 20 mA | Yes |
| Load impedance (in rated range of output) | 110 |
| with voltage outputs, min. with voltage outputs, capacitive load, max. | 1 kΩ 100 nF |
| with voltage outputs, capacitive load, max. with current outputs, max. | 500 Ω |
| with current outputs, inductive load, max. | 1 mH |
| Cable length | |
| • shielded, max. | 200 m |
| | |
| | |
| Analog value generation for the inputs | |
| Analog value generation for the inputs Integration and conversion time/resolution per channel | |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. | 16 bit |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. | 16 bit |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: low | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: low • Step: Medium | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • Step: None • Step: Iow • Step: High | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: Iow • Step: High | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: Iow • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: low • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes Yes 16 bit 1.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: low • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for capacitive load | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes 16 bit 1.5 ms 2.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for inductive load | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes Yes 16 bit 1.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: low • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for inductive load | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes 16 bit 1.5 ms 2.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for inductive load • for inductive load | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes 16 bit 1.5 ms 2.5 ms 2.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for inductive load • for voltage measurement | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes 16 bit 1.5 ms 2.5 ms 2.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for inductive load • for voltage measurement • for voltage measurement | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes 16 bit 1.5 ms 2.5 ms 2.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for voltage measurement • for voltage measurement • for current measurement as 4-wire transducer • for resistance measurement with two-wire | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes 16 bit 1.5 ms 2.5 ms 2.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for voltage measurement • for voltage measurement • for current measurement as 4-wire transducer • for resistance measurement with two-wire connection | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for voltage measurement • for voltage measurement • for current measurement as 4-wire transducer • for resistance measurement with two-wire | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Yes Yes Yes Yes 16 bit 1.5 ms 2.5 ms 2.5 ms |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for inductive load • for voltage measurement • for voltage measurement • for current measurement as 4-wire transducer • for resistance measurement with two-wire connection • for resistance measurement with three-wire | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: None • Step: None • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for capacitive load • for voltage measurement • for current measurement as 4-wire transducer • for resistance measurement with two-wire connection • for resistance measurement with three-wire connection | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes 16 bit 1.5 ms 2.5 ms 2.5 ms Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for resistive load • for voltage measurement • for current measurement as 4-wire transducer • for resistance measurement with two-wire connection • for resistance measurement with three-wire connection • for resistance measurement with three-wire connection | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes Solution 16 bit 1.5 ms 2.5 ms 2.5 ms Yes Yes |
| Analog value generation for the inputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz Smoothing of measured values • parameterizable • Step: None • Step: None • Step: Medium • Step: High Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. Settling time • for resistive load • for capacitive load • for voltage measurement • for voltage measurement • for current measurement as 4-wire transducer • for resistance measurement with two-wire connection • for resistance measurement with three-wire connection • for resistance measurement with four-wire connection | 16 bit Yes; 2.5 / 16.67 / 20 / 100 ms, acts on all channels 400 / 60 / 50 / 10 Yes 16 bit 1.5 ms 2.5 ms 2.5 ms Yes |

| max. | |
|---|---|
| Encoder signals, incremental encoder (asymmetrical) | |
| Input voltage | 24 V |
| Input frequency, max. | 100 kHz |
| Counting frequency, max. | 400 kHz; with quadruple evaluation |
| Signal filter, parameterizable | Yes |
| Incremental encoder with A/B tracks, 90° phase offset | Yes |
| Incremental encoder with A/B tracks, 90° phase offset and zero track | Yes |
| pulse encoder | Yes |
| pulse encoder with direction | Yes |
| pulse encoder with one impulse signal per count direction | Yes |
| Errors/accuracies | |
| Linearity error (relative to input range), (+/-) | 0.1 % |
| Temperature error (relative to input range), (+/-) | 0.005 %/K |
| Crosstalk between the inputs, max. | -60 dB |
| Repeat accuracy in steady state at 25 $^\circ$ C (relative to input range), (+/-) | 0.05 % |
| Output ripple (relative to output range, bandwidth 0 to 50 kHz), (+/-) | 0.02 % |
| Linearity error (relative to output range), (+/-) | 0.15 % |
| Temperature error (relative to output range), (+/-) | 0.005 %/K |
| Crosstalk between the outputs, max. | -80 dB |
| Repeat accuracy in steady state at 25 °C (relative to | 0.05 % |
| output range), (+/-) | |
| Operational error limit in overall temperature range | |
| • Voltage, relative to input range, (+/-) | 0.3 % |
| • Current, relative to input range, (+/-) | 0.3 % |
| • Resistance, relative to input range, (+/-) | 0.3 % |
| Resistance thermometer, relative to input range, (+/-) | Pt100 Standard: ±2 K, Pt100 Climate: ±1 K, Ni100 Standard: ±1.2 K, Ni100 Climate: ±1 K |
| Voltage, relative to output range, (+/-) | 0.3 % |
| Current, relative to output range, (+/-) | 0.3 % |
| Basic error limit (operational limit at 25 °C) | 0.5 % |
| Voltage, relative to input range, (+/-) | 0.2 % |
| • Current, relative to input range, (+/-) | 0.2 % |
| Resistance, relative to input range, (+/-) | 0.2 % |
| • Resistance thermometer, relative to input range, (+/- | Pt100 Standard: ±1 K, Pt100 Climate: ±0.5 K, Ni100 Standard: ±0.6 K, |
|) | Ni100 Climate: ±0.5 K |
| Voltage, relative to output range, (+/-) | 0.2 % |
| • Current, relative to output range, (+/-) | 0.2 % |
| Interference voltage suppression for $f = n x (f1 + /-1 \%)$, $f1 = i$ | interference frequency |
| Series mode interference (peak value of interference < rated value of input range), min. | 30 dB |
| Common mode voltage, max. | 10 V |
| Common mode interference, min. | 60 dB; at 400 Hz: 50 dB |
| Interfaces | |
| Number of PROFINET interfaces | 1 |
| 1. Interface | |
| Interface types | |
| RJ 45 (Ethernet) | Yes; X1 |
| Number of ports | 2 |
| integrated switch | Yes |
| Protocols | |
| IP protocol | Yes; IPv4 |
| PROFINET IO Controller | Yes |
| PROFINET IO Device | Yes |
| SIMATIC communication | Yes |
| Open IE communication | Yes; Optionally also encrypted |
| Web server | Yes |
| Media redundancy | Yes |
| PROFINET IO Controller | |
| Services | |

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| — PG/OP communication | Vac |
|--|---|
| | Yes |
| — Isochronous mode | Yes |
| Direct data exchange | Yes; Requirement: IRT and isochronous mode (MRPD optional) |
| — IRT | Yes |
| — PROFlenergy | Yes; per user program |
| — Prioritized startup | Yes; Max. 32 PROFINET devices |
| Number of connectable IO Devices, max. | 128; In total, up to 256 distributed I/O devices can be connected via AS- i, PROFIBUS or PROFINET |
| — Of which IO devices with IRT, max. | 64 |
| Number of connectable IO Devices for RT, | 128 |
| max. | 100 |
| — of which in line, max. | 128 |
| — Number of IO Devices that can be simultaneously activated (deactivated may | 8; in total across all interfaces |
| simultaneously activated/deactivated, max. | 0 |
| — Number of IO Devices per tool, max. | 8 The minimum value of the undete time also depende on communication |
| — Updating times | The minimum value of the update time also depends on communication share set for PROFINET IO, on the number of IO devices, and on the |
| | quantity of configured user data |
| Update time for IRT | 1 |
| — for send cycle of 250 µs | 250 µs to 4 ms; Note: In the case of IRT with isochronous mode, the |
| | minimum update time of 625 μ s of the isochronous OB is decisive |
| — for send cycle of 500 µs | 500 μ s to 8 ms; Note: In the case of IRT with isochronous mode, the |
| - 7 | minimum update time of $625 \ \mu s$ of the isochronous OB is decisive |
| — for send cycle of 1 ms | 1 ms to 16 ms |
| — for send cycle of 2 ms | 2 ms to 32 ms |
| — for send cycle of 4 ms | 4 ms to 64 ms |
| — With IRT and parameterization of "odd" send | Update time = set "odd" send clock (any multiple of 125 µs: 375 µs, 625 |
| cycles | μs 3 875 μs) |
| Update time for RT | |
| — for send cycle of 250 µs | 250 µs to 128 ms |
| — for send cycle of 500 µs | 500 μs to 256 ms |
| — for send cycle of 1 ms | 1 ms to 512 ms |
| — for send cycle of 2 ms | 2 ms to 512 ms |
| — for send cycle of 4 ms | 4 ms to 512 ms |
| PROFINET IO Device | |
| Services | |
| — PG/OP communication | Yes |
| — Isochronous mode | No |
| — IRT | Yes |
| — PROFlenergy | Yes; per user program |
| — Shared device | Yes |
| — Number of IO Controllers with shared device, | |
| | 4 |
| max. | 4 |
| • | 4 Yes; per user program |
| max. | |
| max. — activation/deactivation of I-devices — Asset management record | Yes; per user program |
| max. — activation/deactivation of I-devices — Asset management record Interface types | Yes; per user program |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) | Yes; per user program Yes; per user program |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps | Yes; per user program Yes; per user program Yes |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation | Yes; per user program Yes; per user program Yes Yes |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing | Yes; per user program Yes; per user program Yes Yes Yes |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing • Industrial Ethernet status LED | Yes; per user program Yes; per user program Yes Yes |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing • Industrial Ethernet status LED Protocols | Yes; per user program Yes; per user program Yes Yes Yes |
| max. | Yes; per user program Yes; per user program Yes Yes Yes Yes Yes |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing • Industrial Ethernet status LED Protocols Number of connections • Number of connections, max. | Yes; per user program Yes; per user program Yes Yes Yes |
| max. | Yes; per user program Yes; per user program Yes Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing • Industrial Ethernet status LED Protocols Number of connections, max. • Number of connections, max. • Number of connections reserved for ES/HMI/web • Number of connections via integrated interfaces | Yes; per user program Yes; per user program Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 |
| max. | Yes; per user program Yes; per user program Yes Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing • Industrial Ethernet status LED Protocols Number of connections, max. • Number of connections, max. • Number of connections reserved for ES/HMI/web • Number of connections via integrated interfaces | Yes; per user program Yes; per user program Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing • Industrial Ethernet status LED Protocols Number of connections, max. • Number of connections, max. • Number of connections reserved for ES/HMI/web • Number of connections via integrated interfaces • Number of S7 routing paths | Yes; per user program Yes; per user program Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) • 100 Mbps • Autonegotiation • Autocrossing • Industrial Ethernet status LED Protocols Number of connections • Number of connections, max. • Number of connections reserved for ES/HMI/web • Number of connections via integrated interfaces • Number of S7 routing paths Redundancy mode | Yes; per user program Yes; per user program Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 16 |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) = 100 Mbps = Autonegotiation = Autocrossing = Industrial Ethernet status LED Protocols Number of connections = Number of connections, max. = Number of connections reserved for ES/HMI/web = Number of connections via integrated interfaces = Number of S7 routing paths Redundancy mode = H-Sync forwarding | Yes; per user program Yes; per user program Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 16 |
| max. — activation/deactivation of I-devices — Asset management record Interface types RJ 45 (Ethernet) = 100 Mbps = Autonegotiation = Autocrossing = Industrial Ethernet status LED Protocols Number of connections = Number of connections, max. = Number of connections reserved for ES/HMI/web = Number of connections via integrated interfaces = Number of S7 routing paths Redundancy mode = H-Sync forwarding Media redundancy | Yes; per user program Yes; per user program Yes Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 16 Yes only via 1st interface (X1) Yes; MRP Automanager according to IEC 62439-2 Edition 2.0, MRP |
| max. | Yes; per user program Yes; per user program Yes Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 16 Yes Only via 1st interface (X1) Yes; MRP Automanager according to IEC 62439-2 Edition 2.0, MRP Manager; MRP Client |
| max. | Yes; per user program Yes; per user program Yes Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 16 Yes Only via 1st interface (X1) Yes; MRP Automanager according to IEC 62439-2 Edition 2.0, MRP Manager; MRP Client Yes; as MRP ring node according to IEC 62439-2 Edition 3.0 |
| max. | Yes; per user program Yes; per user program Yes Yes Yes Yes Yes 128; via integrated interfaces of the CPU and connected CPs / CMs 10 88 16 Yes Only via 1st interface (X1) Yes; MRP Automanager according to IEC 62439-2 Edition 2.0, MRP Manager; MRP Client |

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| Quitebourg times on line breads to a | |
|---|--|
| — Switchover time on line break, typ. | 200 ms; For MRP, bumpless for MRPD |
| — Number of stations in the ring, max. | 50 |
| SIMATIC communication PG/OP communication | Voc: apprintian with TLS V/1.2 pro-palagted |
| S7 routing | Yes; encryption with TLS V1.3 pre-selected Yes |
| S7 communication, as server | Yes |
| S7 communication, as server | Yes |
| User data per job, max. | See online help (S7 communication, user data size) |
| Open IE communication | |
| • TCP/IP | Yes |
| — Data length, max. | 64 kbyte |
| — several passive connections per port, | Yes |
| supported | |
| ISO-on-TCP (RFC1006) | Yes |
| — Data length, max. | 64 kbyte |
| • UDP | Yes |
| — Data length, max. | 2 kbyte; 1 472 bytes for UDP broadcast |
| — UDP multicast | Yes; Max. 5 multicast circuits |
| • DHCP | Yes |
| • DNS | Yes |
| • SNMP | Yes |
| • DCP | Yes |
| • LLDP | Yes |
| Encryption | Yes; Optional |
| Web server HTTP | Yes; Standard and user pages |
| • HTTPS | Yes; Standard and user pages |
| OPC UA | |
| Runtime license required | Yes; "Small" license required |
| OPC UA Client | Yes |
| — Application authentication | Yes |
| — Security policies | Available security policies: None, Basic128Rsa15, Basic256Rsa15, |
| | Basic256Sha256 |
| — User authentication | "anonymous" or by user name & password |
| - Number of connections, max. | 4 |
| Number of nodes of the client interfaces, recommended max. | 1 000 |
| — Number of elements for one call of | 300 |
| OPC_UA_NodeGetHandleList/OPC_UA_ReadList/C | 300 |
| max. | |
| - Number of elements for one call of | 20 |
| OPC_UA_NameSpaceGetIndexList, max. | 400 |
| — Number of elements for one call of OPC UA MethodGetHandleList, max. | 100 |
| — Number of simultaneous calls of the client | 1 |
| instructions for session management, per | |
| connection, max. | |
| - Number of simultaneous calls of the client | 5 |
| instructions for data access, per connection, max. | |
| — Number of registerable nodes, max. | 5 000 |
| — Number of registerable method calls of OPC_UA_MethodCall, max. | 100 |
| — Number of inputs/outputs when calling | 20 |
| OPC_UA_MethodCall, max. | |
| OPC UA Server | Yes; Data access (read, write, subscribe), method call, custom address |
| | space |
| — Application authentication | Yes |
| — Security policies | Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256 |
| — User authentication | "anonymous" or by user name & password |
| GDS support (certificate management) | Yes |
| — Number of sessions, max. | 32 |
| Number of accessible variables, max. | 50 000 |
| — Number of registerable nodes, max. | 10 000 |
| Number of subscriptions per session, max. | 20 |
| — Sampling interval, min. | 100 ms |
| — Publishing interval, min. | 500 ms |

| — Number of server methods, max. | 20 |
|--|--|
| Number of inputs/outputs per server method, | 20 |
| max. | |
| Number of monitored items, recommended max. | 1 000; for 1 s sampling interval and 1 s send interval |
| — Number of server interfaces, max. | 10 of each "Server interfaces" / "Companion specification" type and 20 of the type "Reference namespace" |
| — Number of nodes for user-defined server interfaces, max. | 1 000 |
| Alarms and Conditions | Yes |
| — Number of program alarms | 100 |
| — Number of alarms for system diagnostics | 50 |
| Further protocols | |
| MODBUS | Yes; MODBUS TCP |
| Isochronous mode | |
| | |
| Equidistance | Yes |
| S7 message functions | |
| Number of login stations for message functions, max. | 32 |
| Program alarms | Yes |
| Number of configurable program messages, max. | 5 000; Program messages are generated by the "Program_Alarm" block, ProDiag or GRAPH |
| Number of loadable program messages in RUN, max. | 2 500 |
| Number of simultaneously active program alarms | |
| Number of program alarms | 600 |
| Number of program diamits Number of alarms for system diagnostics | 100 |
| Number of alarms for system alagnosites Number of alarms for motion technology objects | 80 |
| | |
| Test commissioning functions | |
| Joint commission (Team Engineering) | Yes; Parallel online access possible for up to 5 engineering systems |
| Status block | Yes; Up to 8 simultaneously (in total across all ES clients) |
| Single step | No |
| Number of breakpoints | 8 |
| Status/control | |
| Status/control variable | Yes |
| Variables | Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters |
| Number of variables, max. | |
| — of which status variables, max. | 200; per job |
| - of which control variables, max. | 200; per job |
| Forcing | |
| Forcing | Yes |
| Forcing, variables | Peripheral inputs/outputs |
| Number of variables, max. | 200 |
| Diagnostic buffer | 200 |
| present | Yes |
| | |
| Number of entries, max. | 1 000 |
| — of which powerfail-proof | 500 |
| Traces | |
| Number of configurable Traces | 4; Up to 512 KB of data per trace are possible |
| Interrupts/diagnostics/status information | |
| Alarms | |
| Diagnostic alarm | Yes |
| Hardware interrupt | Yes |
| Diagnoses | |
| Monitoring the supply voltage | Yes |
| Wire-break | Yes; for analog inputs/outputs, see description in manual |
| Short-circuit | Yes; for analog outputs, see description in manual |
| A/B transition error at incremental encoder | Yes |
| Diagnostics indication LED | |
| RUN/STOP LED | Yes |
| • ERROR LED | Yes |
| MAINT LED | |
| | Yes |
| • STOP ACTIVE LED | Yes |
| Monitoring of the supply voltage (PWR-LED) | |
| | Yes |
| Channel status display for channel diagnostics | Yes Yes Yes; For analog inputs/outputs |

| Connection display LINK TX/RX | Yes |
|--|---|
| Supported technology objects | |
| Motion Control | Yes; Note: The number of technology objects affects the cycle time of |
| Number of available Motion Control resources for technology objects | the PLC program; selection guide via the TIA Selection Tool 800 |
| Required Motion Control resources | |
| — per speed-controlled axis | 40 |
| — per positioning axis | 80 |
| — per synchronous axis | 160 |
| — per external encoder | 80 |
| — per output cam | 20 |
| — per cam track | 160 |
| — per probe | 40 |
| Positioning axis — Number of positioning axes at motion control | 5 |
| cycle of 4 ms (typical value) — Number of positioning axes at motion control cycle of 8 ms (typical value) | 10 |
| Controller | |
| PID_Compact | Yes; Universal PID controller with integrated optimization |
| PID_3Step | Yes; PID controller with integrated optimization for valves |
| • PID-Temp | Yes; PID controller with integrated optimization for temperature |
| Counting and measuring | |
| High-speed counter | Yes |
| Integrated Functions | |
| Counting functions | |
| Continuous counting | Yes |
| Counter response parameterizable | Yes |
| Hardware gate via digital input | Yes |
| Software gate | Yes |
| Event-controlled stop | Yes |
| Synchronization via digital input | Yes |
| Counting range, parameterizable | Yes |
| Comparator | |
| — Number of comparators | 2; per count channel; see manual for details |
| — Direction dependency | Yes |
| — Can be changed from user program | Yes |
| Position detection | |
| Incremental acquisition | Yes |
| Suitable for S7-1500 Motion Control | Yes |
| Measuring functions | |
| Measuring time, parameterizable | Yes |
| Dynamic measurement period adjustment | Yes |
| Number of thresholds, parameterizable | 2 |
| Measuring range | |
| — Frequency measurement, min. | 0.04 Hz |
| — Frequency measurement, max. | 400 kHz; with quadruple evaluation |
| - Cycle duration measurement, min. | 2.5 μs |
| — Cycle duration measurement, max. | 25 s |
| Accuracy Fraguency mossurement | 100 ppm; depending on measuring interval and signal such stars |
| — Frequency measurement Cycle duration measurement | 100 ppm; depending on measuring interval and signal evaluation |
| — Cycle duration measurement | 100 ppm; depending on measuring interval and signal evaluation |
| - Velocity measurement | 100 ppm; depending on measuring interval and signal evaluation |
| Potential separation | |
| Potential separation digital inputs | No |
| between the channels between the channels in groups of | No 16 |
| between the channels, in groups of | 16 |
| Potential separation digital outputs • between the channels | No |
| | No 16 |
| between the channels, in groups of Potential separation channels | |
| between the channels and backplane bus | Yes |
| Between the channels and load voltage L+ | No |
| · Detween the sharmers and load voltage LT | |

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| Isolation | |
|---|--|
| Isolation tested with | 707 V DC (type test) |
| Ambient conditions | |
| Ambient temperature during operation | |
| horizontal installation, min. | -25 °C; No condensation |
| horizontal installation, max. | 60 °C; note derating data for onboard I/O in the manual. Display: 50 °C, at an operating temperature of typically 50 °C, the display is switched off |
| vertical installation, min. | -25 °C; No condensation |
| vertical installation, max. | 40 °C; note derating data for onboard I/O in the manual. Display: 40 °C, at an operating temperature of typically 40 °C, the display is switched off |
| Ambient temperature during storage/transportation | |
| ● min. | -40 °C |
| • max. | 70 °C |
| Altitude during operation relating to sea level | |
| Installation altitude above sea level, max. | 5 000 m; Restrictions for installation altitudes > 2 000 m, see manual |
| configuration / header | |
| configuration / programming / header | |
| Programming language | |
| — LAD | Yes |
| — FBD | Yes |
| — STL | Yes |
| — SCL | Yes |
| — GRAPH | Yes |
| Know-how protection | |
| User program protection/password protection | Yes |
| Copy protection | Yes |
| Block protection | Yes |
| Access protection | |
| protection of confidential configuration data | Yes |
| Password for display | Yes |
| Protection level: Write protection | Yes |
| Protection level: Read/write protection | Yes |
| Protection level: Complete protection | Yes |
| Dimensions | |
| Width | 110 mm |
| Height | 147 mm |
| Depth | 129 mm |
| Weights | |
| Weight, approx. | 1 360 g |
| last modified: | 11/3/2021 🖸 |